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minmax optimization constraint

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1 Generalized constraint generation in the presence of non-deterministic parasiticsEdoardo Charbon, Paolo Miliuzzi, Enrico Malavasi, Alberto L. Sangiovanni-Vincentelli
January 1997 **Proceedings of the 1996 IEEE/ACM international conference on Computer-aided design**Full text available: [pdf\(137.48 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)[Publisher Site](#)

In a constraint-driven layout synthesis environment, parasitic constraints are generated and implemented in each phase of the design process to meet a given set of performance specifications. The success of the synthesis phase depends in great part on the effectiveness and the generality of the constraint generation process. None of the existing approaches to the constraint generation problem however are suitable for a number of parasitic effects in active and passive devices due to non-deterministic parasitics

Keywords: constraint-driven layout synthesis, constraint generation, non-deterministic parasitics

2 Algorithm 811: NDA: algorithms for nondifferentiable optimization

Ladislav Lukšan, Jan Vlček

June 2001 **ACM Transactions on Mathematical Software (TOMS)**, Volume 27 Issue 2Full text available: [pdf\(162.19 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

We present four basic Fortran subroutines for nondifferentiable optimization with simple bounds and general linear constraints. Subroutine PMIN, intended for minimax optimization, is based on a sequential quadratic programming variable metric algorithm. Subroutines PBUN and PNEW, intended for general nonsmooth problems, are based on bundle-type methods. Subroutine PVAR is based on special nonsmooth variable metric methods. Besides the description of methods and codes, we propose computation ...

Keywords: discrete Chebyshev approximation, general linear constraints, minimax optimization, sequential quadratic programming methods, variable metric methods

3 [Session 6A: Equitable cost allocations via primal-dual-type algorithms](#)

Kamal Jain, Vijay V. Vazirani

May 2002 **Proceedings of the thirty-fourth annual ACM symposium on Theory of computing**

Full text available: [pdf\(219.75 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Perhaps the strongest notion of truth-revealing in a cost sharing method is group strategyproofness. However, matters are not so clear-cut on fairness, and many different, sometimes even conflicting, notions of fairness have been proposed which have relevance in different situations. We present a large class of group strategyproof cost sharing methods, for submodular cost functions, satisfying a wide range of fairness criteria, thereby allowing the service provider to choose a method that best s ...

4 [An updated survey of GA-based multiobjective optimization techniques](#)

Carlos A. Coello

June 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 2

Full text available: [pdf\(250.77 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

After using evolutionary techniques for single-objective optimization during more than two decades, the incorporation of more than one objective in the fitness function has finally become a popular area of research. As a consequence, many new evolutionary-based approaches and variations of existing techniques have recently been published in the technical literature. The purpose of this paper is to summarize and organize the information on these current approaches, emphasizing the importance ...

Keywords: artificial intelligence, genetic algorithms, multicriteria optimization, multiobjective optimization, vector optimization

5 [Test scheduling for core-based systems](#)

Krishnendu Chakrabarty

November 1999 **Proceedings of the 1999 IEEE/ACM international conference on Computer-aided design**

Full text available: [pdf\(136.90 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present optimal solutions to the test scheduling problem for core-based systems. We show that test scheduling is equivalent to the m-processor open-shop scheduling problem and is therefore NP-complete. However, a commonly-encountered instance of this problem ($m = 2$) can be solved in polynomial time. For the general case ($m > 2$), we present a mixed-integer linear programming (MILP) model for optimal scheduling and apply it to a representative core-based system using an MILP sol ...

6 [Towards a syntactic characterization of PTAS](#)

Sanjeev Khanna, Rajeev Motwani

July 1996 **Proceedings of the twenty-eighth annual ACM symposium on Theory of computing**

Full text available: [pdf\(1.01 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

7 [New perspectives in physical design: Congestion-driven codesign of power and signal networks](#)

Haihua Su, Jiang Hu, Sachin S. Sapatnekar, Sani R. Nassif

June 2002 **Proceedings of the 39th conference on Design automation**

Full text available: [PDF\(200.53 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present a global wire design methodology that simultaneously considers the performance needs for both signal lines and power grids under congestion considerations. An iterative procedure is employed in which the global routing is performed according to a congestion map that includes the resource utilization of the power grid, followed by a step in which the power grid is adjusted to relax the congestion in crowded regions. This adjustment is in the form of wire removal in noncritical regions, ...

Keywords: codesign, power grid noise, signal routing, wire congestion

8 [Session 5A: Embedded tutorial: embedded software and systems: Low power system scheduling and synthesis](#)

Niraj K. Jha

November 2001

Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design

Full text available: [PDF\(168.32 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Many scheduling techniques have been presented recently which exploit dynamic voltage scaling (DVS) and dynamic power management (DPM) for both uniprocessors and distributed systems, as well as both real-time and non-real-time systems. While such techniques are power-aware and aim at extending battery lifetimes for portable systems, they need to be augmented to make them battery-aware as well. We will survey such power-aware and battery-aware scheduling algorithms. Also, system synthesis algorithm ...

9 [Active learning of label ranking functions](#)

Klaus Brinker

July 2004

Twenty-first international conference on Machine learning

Full text available: [PDF\(219.43 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#)

The effort necessary to construct labeled sets of examples in a supervised learning scenario is often disregarded, though in many applications, it is a time-consuming and expensive procedure. While this already constitutes a major issue in classification learning, it becomes an even more serious problem when dealing with the more complex target domain of total orders over a set of alternatives. Considering both the pairwise decomposition and the constraint classification technique to represent l ...

10 [Summaries of MobiHoc 2003 posters: Distributed topology control mechanism for mobile Ad hoc networks with swarm intelligence](#)

Zhuochuan Huang, Chien-Chung Shen

July 2003

ACM SIGMOBILE Mobile Computing and Communications Review, Volume 7 Issue 3